

**NOTES:**

1. A CONFIGURATION SWITCH (NOT SHOWN) IS LOCATED ON THIS UNIT. SWITCH 1 AND 2 ADJUST THE NUMBER OF INPUT CHANNELS. SWITCH 3 ENABLES/DISABLES INPUT CHANNEL #1. WHEN SETTING THE NUMBER OF INPUT CHANNELS, INPUT #1 MUST BE COUNTED EVEN IF IT IS DISABLED BY SWITCH 3.
2. THE CONFIGURATION SWITCH IS ONLY READ DURING POWER-UP. IF THE SWITCH IS CHANGED, POWER MUST BE REMOVED FOR 15 SECONDS AND THEN REAPPLIED FOR THE NEW SWITCH SETTINGS TO WORK.

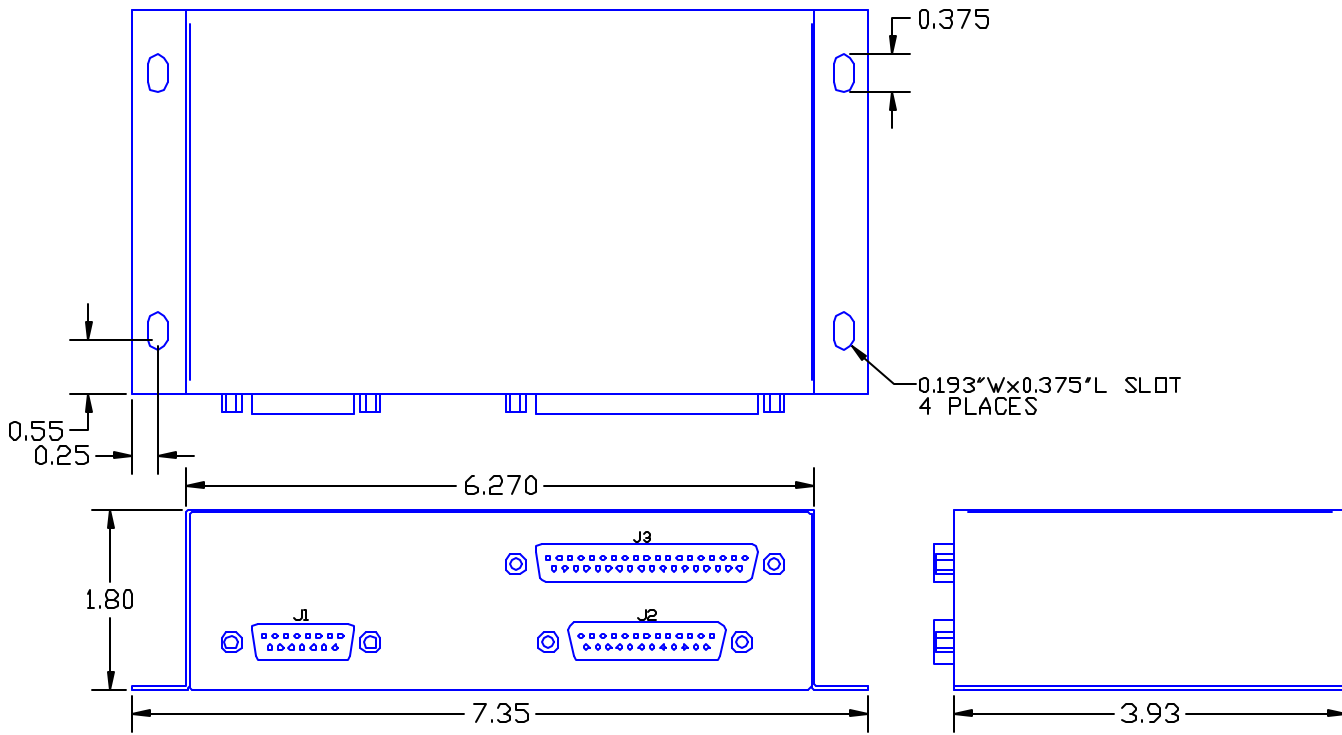
REVISIONS			
REV	DESCRIPTION	DATE	APPR

J3 CONNECTOR DESCRIPTION	
PART NUMBER	DESCRIPTION
MD3/MSRSNTD	CONNECTOR
WIRE DESCRIPTION	
PIN	DESCRIPTION
1	+28V
2	GND
3	VF-CLK
4	PA KEY INPUT (GND IN)
5	#2 SELECT (MON. GND IN)
6	#4 SELECT (MON. GND IN)
7	#6 SELECT (MON. GND IN)
8	#8 SELECT (MON. GND IN)
9	#10 SELECT (MON. GND IN)
10	CHANNEL 1 - STATUS BIT 1
11	CHANNEL 2 - STATUS BIT 1
12	CHANNEL 3 - STATUS BIT 1
13	CHANNEL 4 - STATUS BIT 1
14	CHANNEL 5 - STATUS BIT 1
15	CHANNEL 6 - STATUS BIT 1
16	CHANNEL 7 - STATUS BIT 1
17	CHANNEL 8 - STATUS BIT 1
18	CHANNEL 9 - STATUS BIT 1
19	CHAN. 10 - STATUS BIT 1
20	CHINE-MUTE (+28V IN)
21	VF-DATA
22	CHINE-MUTE (GND IN)
23	#1 SELECT (MON. GND IN)
24	#3 SELECT (MON. GND IN)
25	#5 SELECT (MON. GND IN)
26	#7 SELECT (MON. GND IN)
27	#9 SELECT (MON. GND IN)
28	CHANNEL 1 - STATUS BIT 0
29	CHANNEL 2 - STATUS BIT 0
30	CHANNEL 3 - STATUS BIT 0
31	CHANNEL 4 - STATUS BIT 0
32	CHANNEL 5 - STATUS BIT 0
33	CHANNEL 6 - STATUS BIT 0
34	CHANNEL 7 - STATUS BIT 0
35	CHANNEL 8 - STATUS BIT 0
36	CHANNEL 9 - STATUS BIT 0
37	CHAN. 10 - STATUS BIT 0

J2 CONNECTOR DESCRIPTION	
PART NUMBER	DESCRIPTION
MD25MSRSNTD	CONNECTOR
WIRE DESCRIPTION	
PIN	DESCRIPTION
1	#1 LEFT AUDIO OUTPUT
2	#2 LEFT AUDIO OUTPUT
3	#3 LEFT AUDIO OUTPUT
4	#4 LEFT AUDIO OUTPUT
5	#5 LEFT AUDIO OUTPUT
6	#6 LEFT AUDIO OUTPUT
7	#7 LEFT AUDIO OUTPUT
8	#8 LEFT AUDIO OUTPUT
9	#9 LEFT AUDIO OUTPUT
10	#10 LEFT AUDIO OUTPUT
11	AUDIO COMMON
12	AUDIO COMMON
13	NO CONNECTION
14	#1 RIGHT AUDIO OUTPUT
15	#2 RIGHT AUDIO OUTPUT
16	#3 RIGHT AUDIO OUTPUT
17	#4 RIGHT AUDIO OUTPUT
18	#5 RIGHT AUDIO OUTPUT
19	#6 RIGHT AUDIO OUTPUT
20	#7 RIGHT AUDIO OUTPUT
21	#8 RIGHT AUDIO OUTPUT
22	#9 RIGHT AUDIO OUTPUT
23	#10 RIGHT AUDIO OUTPUT
24	AUDIO COMMON
25	AUDIO COMMON

J1 CONNECTOR DESCRIPTION	
PART NUMBER	DESCRIPTION
MD15MSRSNTD	CONNECTOR
WIRE DESCRIPTION	
PIN	DESCRIPTION
1	CHANNEL 1 LEFT AUDIO *
2	CHANNEL 1 AUDIO LOW
3	CHANNEL 2 LEFT AUDIO *
4	CHANNEL 2 AUDIO LOW
5	CHANNEL 3 LEFT AUDIO *
6	CHANNEL 4 AUDIO LOW
7	CHANNEL 4 LEFT AUDIO *
8	PA AUDIO INPUT - **
9	CHANNEL 1 RIGHT AUDIO *
10	N/C ***
11	CHANNEL 2 RIGHT AUDIO *
12	CHANNEL 3 AUDIO LOW
13	CHANNEL 3 RIGHT AUDIO *
14	PA AUDIO INPUT + **
15	CHANNEL 4 RIGHT AUDIO *

\* LINE LEVEL INPUTS:  
0.5Vpp - 15Vpp  
AUDIO COUPLING TRANSFORMER  
IMPEDANCE = 10K OHMS  
\*\* PA AUDIO INPUT IS AN AUDIO  
COUPLING TRANSFORMER.  
IMPEDANCE = 600 OHMS  
\*\*\* PINS MARKED 'N/C' ARE  
GROUNDED INTERNALLY.



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Mid Continent Controls, Inc. (316)789-0088  
219 S. Water, Derby, KS 67037

FINISH: BLACK ANODIZE		4x10 AUDIO SWITCHER/DISTRIBUTION AMPLIFIER		
MATERIAL: N/A	SIZE A	DRAWN DLS	DWG NO. 102008	REV IR
TOLERANCE: ±0.010" UNLESS NOTED	SCALE N/A	DATE 1/15/99	SHEET 1 OF 1	